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From: **Sumit Bhattacharya** Date: **March 17, 2005**
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Docket Number: **2207/10083** Total number of pages: **5**
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Ex. Guy J. LAMARRE Group Art Unit 2133	U.S. Patent and Trademark Office	703-872-9306	

Message:

Application No. : 09/751,750 Confirmation No. 5033
Applicant : **Talal K. JABER**
Filed : **December 29, 2000**
Title : **MICROPROCESSOR ON-CHIP TESTING ARCHITECTURE AND
IMPLEMENTATION**
TC/A.U. : **2133**
Examiner : **Guy J. LAMARRE**

PAPER(s) ENTITLED: **Notice of Appeal (in duplicate)** 2 pages
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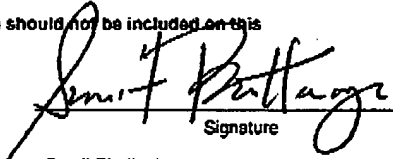
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Signature <u>Pilar Rodriguez</u> Typed or printed name <u>Pilar Rodriguez</u>		Application Number 09/751,750	Filed December 29, 2000
		For Microprocessor On-Chip Testing Architecture And Implementation	
		Art Unit 2133	Examiner Guy J. LAMARRE
Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the examiner.			
The fee for this Notice of Appeal is (37 CFR 1.17(b)) \$ 500.00			
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<input type="checkbox"/> assignee of record of the entire interest. See 37 CFR 3.71.		Sumit Bhattacharya	
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<input checked="" type="checkbox"/> attorney or agent of record.		(408) 975-7500	
Registration number <u>51,468</u>		Telephone number	
<input type="checkbox"/> attorney or agent acting under 37 CFR 1.34(a).		March 17, 2005	
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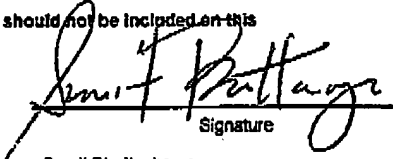
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 Signature <u>Sumit Bhattacharya</u> Typed or printed name <u>(408) 875-7500</u> Telephone number <u>March 17, 2005</u> Date			
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